Search Results -

Terms	Documents
L2 same controller	39

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

L3

Refine Search

Recall Fext
Clear

Interrupt

Search History

DATE: Tuesday, August 23, 2005 Printable Copy Create Case

Set Name side by side	Query	Hit Count	<u>Set</u> <u>Name</u> result set
DB=Pc	GPB,USPT,USOC; PLUR=YES; OP=OR		
<u>L3</u>	L2 same controller	39	<u>L3</u>
<u>L2</u>	L1 same (chip or IC)	79	<u>L2</u>
<u>L1</u>	(memory adj1 (card or board)) same (interfac\$3 near5 (USB or "universal serial bus"))	944	<u>L1</u> .

Search Results -

Terms	Documents
L3 and L6	0

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

L10

Recall Text
Clear
Interrupt

Search History

DATE: Tuesday, August 23, 2005 Printable Copy Create Case

Set Name side by side	Query	<u>Hit</u> <u>Count</u>	Set Name result set
$DB=U_{i}$	SPT; PLUR=YES; OP=OR		
<u>L10</u>	13 and L6	0	<u>L10</u>
<u>L9</u>	l4 and L6	0	<u>L9</u>
<u>L8</u>	15 and L6	0	<u>L8</u>
<u>L7</u>	15 same L6	0	<u>L7</u>
<u>L6</u>	(memory adj1 (card or board)) same (interfac\$3 near5 (USB or "universal serial bus"))	170	<u>L6</u>
<u>L5</u>	memory near3 (single adj1 (chip or IC))	1628	<u>L5</u>
<u>L4</u>	memory near3 (single adj1 (chip or IC or "integrated circuit"))	2114	<u>L4</u>
<u>L3</u>	memory near5 (single adj1 (chip or IC or "integrated circuit"))	3384	<u>L3</u>
<u>L2</u>	L1 and (chip or IC or "integrated circuit")	1	<u>L2</u>
<u>L1</u>	6725286.pn.	1	<u>L1</u>

Search Results -

Terms	Documents
L1 same L2	29

Database:

US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

US Pre-Grant Publication Full-Text Database

Search:

<u>.</u>	

Recall Text Clear

Refine Search

Interrupt

Search History

DATE: Tuesday, August 23, 2005 Printable Copy Create Case

	<u>e Query</u>	Hit Count	
ide by sid	е		result set
DB=P	GPB, USPT, USOC; PLUR = YES; OP = OR		·
<u>L3</u>	11 same L2	29	<u>L3</u>
<u>L2</u>	(card or board) same interfac\$3 same (USB or "universal serial bus") same controller	1982	<u>L2</u>
<u>L1</u>	memory same (single adj1 (chip or IC or "integrated circuit"))	14680	<u>L1</u>

Search Results -

Terms	Documents
L3	0

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

L4

Refine Search

Refine Search

Search History

DATE: Tuesday, August 23, 2005 Printable Copy Create Case

Set Name side by side	- · · · ·	Hit Count	Set Name result set
DB=EI	PAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L4</u>	13	0	<u>L4</u>
DB=PC	GPB,USPT,USOC; PLUR=YES; OP=OR		
<u>L3</u>	11 same L2	29	<u>L3</u>
<u>L2</u>	(card or board) same interfac\$3 same (USB or "universal serial bus") same controller	1982	<u>L2</u>
<u>L1</u>	memory same (single adj1 (chip or IC or "integrated circuit"))	14680	<u>L1</u>

Search Results -

Terms	Documents
L1 same (single adj1 (chip or IC))	2

US Pre-Grant Publication Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

L5

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Tuesday, August 23, 2005 Printable Copy Create Case

Set Name side by side	Query	Hit Count	<u>Set</u> <u>Name</u> result set
DB=PC	GPB, USPT, USOC; PLUR=YES; OP=OR	4	
<u>L5</u>	L1 same (single adj1 (chip or IC))	2	<u>L5</u>
DB=EI	$PAB,JPAB,DWPI,TDBD;\ PLUR=YES;\ OP=OR$		
<u>L4</u>	L3	0	<u>L4</u>
DB=PC	GPB, USPT, USOC; PLUR = YES; OP = OR		
· <u>L3</u>	L2 same controller	39	<u>L3</u>
<u>L2</u>	L1 same (chip or IC)	79	<u>L2</u>
<u>L1</u>	(memory adj1 (card or board)) same (interfac\$3 near5 (USB or "universal serial bus"))	944	<u>L1</u>

Search Results -

Terms	Documents
L3	0

Database:

Database:

Database:

Database:

Database:

EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

L4

Search:

Recall Text
Clear

Interrupt

Search History

DATE: Tuesday, August 23, 2005 Printable Copy Create Case

Na side	et me e by de	Query	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
DI	B=E	PAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>I</u>	<u>.4</u>	L3	0	<u>L4</u>
DI	B=P	GPB,USPT,USOC; PLUR=YES; OP=OR		
<u>T</u>	<u>.3</u>	L2 same controller	39	<u>L3</u>
<u>I</u>	<u>.2</u>	L1 same (chip or IC)	79	<u>L2</u>
<u>L</u>	<u>.1</u>	(memory adj 1 (card or board)) same (interfac\$3 near5 (USB or "universal serial bus"))	944	<u>L1</u>

Search Results -

Terms	Documents
(439/59 439/79 439/945 361/684 361/686 361/737 710/300 710/301 710/302 710/303 710/62 710/74 710/313 710/316 711/100 711/103).ccls.	9725

US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database Database: **EPO Abstracts Database** JPO Abstracts Database **Derwent World Patents Index** IBM Technical Disclosure Bulletins L1 Search: Refine Search Recall Text 4 Interrupt Clear

Search History

DATE: Tuesday, August 23, 2005 Printable Copy Create Case

Set Name Query side by side

Hit Count Set Name

result set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

Ll 710/300-303,62,74,313,316;711/100,103;361/684,686,737;439/59,79,945.ccls. 9725 <u>L1</u>

Search Results -

Terms	Documents	
L1 and L3	16	

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Recall Text 🚄

Search:

L4	

Refine Search

Interrupt

Search History

Clear

DATE: Tuesday, August 23, 2005 Printable Copy Create Case

Set Name side by side	Query	<u>Hit</u> <u>Count</u>	Set Name result set
DB=P0	GPB,USPT,USOC; PLUR=YES; OP=OR		
<u>L4</u>	11 and L3	16	<u>L4</u>
<u>L3</u>	L2 same (chip or IC or "integrated circuit")	84	<u>L3</u>
<u>L2</u>	(memory adj1 (card or board)) same (interfac\$3 near5 (USB or "universal serial bus"))	944	<u>L2</u>
<u>L1</u>	710/300-303,62,74,313,316;711/100,103;361/684,686,737;439/59,79,945.ccls.	9725	<u>L1</u>